

Curriculum Vitae

Prof. Dr.-Ing. Max Christian Lemme

Heisenberg Professor for Graphene-based Nanotechnology

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Work experience

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|-----------------|---|
| Since 11/2012 | Full Professor (W3), University of Siegen, Siegen, Germany |
| Since 09/2014 | Visiting Professor at CEA LETI, Grenoble, France |
| Since 06/2014 | Affiliated Professor, KTH Royal Institute of Technology, Stockholm, Sweden |
| 09/2010-08/2013 | Guest Professor, KTH Royal Institute of Technology, Stockholm, Sweden |
| 07/2008-07/2010 | Research Fellow, Harvard University, Cambridge, USA |
| 11/1998-06/2008 | AMO GmbH, Aachen, last position: Head of Technology Department AMICA (Advanced Microelectronic Center Aachen) |
| 08/1998-10/1998 | Researcher at Institute for Semiconductor Electronics, RWTH-Aachen University, Aachen, Germany |
| 05/1996-07/1996 | Internship: Denso, Karya City, Japan, Thema: „Adhesion measurements of thin films“ |
| 06/1993-08/1993 | Internship: National Semiconductor, Arlington, USA, Thema: „Qualification Cost of CS80 Production“ |
| 05/1993-12/1996 | Founder: Kalle Computer GbR, Consulting on Computer Hard- and Software |

Awards

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| 2012 | Starting Grant, ERC (Consolidator, European Research Council) |
| 2012 | Heisenberg Professorship, German Research Foundation, (DFG, Deutsche Forschungsgemeinschaft) |
| 2007 | Feodor Lynen-Research Fellow, Alexander von Humboldt-Foundation |
| 2006 | Young researcher award „NanoFutur“ (German Federal Ministry of Education and Research, BMBF) |
| 2006 | Senior Member IEEE |
| 2004 | Borchersplakette, RWTH-Aachen University |

Research Management

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|--------------|---|
| Coordinator: | GRADE (Graphene-based Devices and Circuits for RF Applications), FP7 STREP, 01.10.2012-30.09.2015 |
| Coordinator: | GRAND (Graphene-based Nanoelectronic Devices), FP7 STREP, 01.01.2008-31.12.2010 |

Publications

Number: >75 (ISI Web of Science)

H-Index 20 (ISI Web of Science)

Teaching Experience

Since 2013 Lecture: Nanotechnology, University of Siegen

2011 Lecture: Design and Characterisation of Nano- and Microdevices, 7.5 hp (higher education credits), KTH, Sweden, course responsible

Committee- und Review-Activities

2014-2015 Member of Technical Program Committee of **IEDM** conference (International Electron Device Meeting)

2013-2015 Member of Technical Programme Committee of **DRC** conference (Device Research Conference) and IEEE SNW Workshop (Silicon Nanoelectronics Workshop)

Since 2012 Member of Scientific Advisory Board, Faculty of Natural Sciences and Engineering, University of Siegen

Since 2010 Project reviewer for European Commission FP7. H2020, COST

Since 2010 Member of Technical Programme Committee of **ESSDERC** conference (European Solid-State Device Research Conference)

2009 Conference Chair / Organizer of **ULIS** conference (International Conference on Ultimate Integration of Silicon), Aachen, 2009

2008-2009 Member of Technical Program Committee of **IEDM** conference (International Electron Device Meeting), Session Chair "Solid State and Nanoelectronic Devices" in 2008 and 2009.

Since 2007 Member of Technical Program Committee of **ULIS** conference

Since 2000 Regular reviews for Nature Materials, Nature Nanotechnology, Nano Letters, ACS Nano, IEEE Electron Device Letters & Transactions on Electron Devices, (2007 - 2011 IEEE Golden List of Reviewers), Applied Physics Letters, Journal of Applied Physics, Solid State Electronics, Microelectronic Engineering

Education

07/2003 **Dissertation (Dr.-Ing.):** „Entwicklung eines Triple-Gate CMOS-Prozesses auf SOI-Material“,

07/1998 **Diploma Thesis (Dipl.-Ing.):** „Neuartige UMOS-Feldeffekttransistoren auf Siliciumcarbid (SiC)“

07/1998 Electrical Engineering, RWTH-Aachen, Diplom-Ingenieur